

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1-2. (Cancelled)

3. (Previously Presented) A RAM-incorporated driver that drives a display section, based on a command from an external MPU, and still-image data and moving-image data, the RAM-incorporated driver comprising:

a first bus line that transfers the still-image data from the external MPU;

a second bus line that transfers the moving-image data from the external MPU;

a RAM that stores the still-image data and the moving-image data;

a first column address control circuit that specifies a column address in the RAM for writing the still-image data;

a second column address control circuit that specifies a column address in the RAM for writing the moving-image data;

a first page address control circuit that specifies a page address in the RAM for writing the still-image data;

a second page address control circuit that specifies a page address in the RAM for writing the moving-image data;

an MPU-related control circuit that controls the first and second column address control circuits and the first and second page address control circuits, based on the command from the external MPU;

a display address control circuit that controls reading of the still-image data and the moving-image data stored in the RAM, as display data; and

a driver-related control circuit that controls the display address control circuit independently of the MPU-related control circuit.

4. (Previously Presented) The RAM-incorporated driver as defined by claim 3, further comprising:

- a first pair of bit lines connected to a memory cell of the RAM;
- a second pair of bit lines connected to a memory cell of the RAM;
- a first column switch controlled by the first column address control circuit and connecting the first pair of bit lines with the first bus line;
- a second column switch controlled by the second column address control circuit and connecting the second pair of bit lines with the second bus line;
- a first word line which transfers a signal from the first page address control circuit to a control terminal of a first switch provided between a memory element within the memory cell and the first pair of bit lines; and
- a second word line which transfers a signal from the second page address control circuit to a control terminal of a second switch provided between the memory element and the second pair of bit lines.

5. (Previously Presented) The RAM-incorporated driver as defined by claim 4: wherein the RAM has a first RAM which stores the still-image data that has been transferred via the first bus line and a second RAM which stores the moving-image data that has been transferred via the second bus line,

wherein the display address control circuit further comprises a first display address control circuit which controls the reading of still-image data from the first RAM as display data and a second display address control circuit which controls the reading of moving-image from the second RAM as display data,

wherein, in the first RAM, write addresses are controlled by the first column address control circuit and the first page address control circuit, and

wherein, in the second RAM, write addresses are controlled by the second column address control circuit and the second page address control circuit.

6. (Previously Presented) The RAM-incorporated driver as defined by claim 3:
wherein the first column address control circuit specifies a read column address in the RAM based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies a read page address in the RAM based on a signal from the MPU-related control circuit.

7. (Previously Presented) The RAM-incorporated driver as defined by claim 4:
wherein the first column address control circuit specifies a read column address in the RAM based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies a read page address in the RAM based on a signal from the MPU-related control circuit.

8. (Previously Presented) The RAM-incorporated driver as defined by claim 5:
wherein the first column address control circuit specifies a read column address in the RAM based on a signal from the MPU-related control circuit, and

wherein the first page address control circuit specifies a read page address in the RAM based on a signal from the MPU-related control circuit.

9-10. (Cancelled)

11. (Original) A display unit comprising:
a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;
the RAM-incorporated driver, as defined by claim 3, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

12. (Original) A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 4, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

13. (Original) A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 5, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

14. (Original) A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 6, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

15. (Original) A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 7, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

16. (Original) A display unit comprising:

a panel having an electro-optical element driven by a plurality of first electrodes and a plurality of second electrodes;

the RAM-incorporated driver, as defined by claim 8, which drives the plurality of first electrodes; and

a scanning driver which scans and drives the plurality of second electrodes.

17-25. (Cancelled)